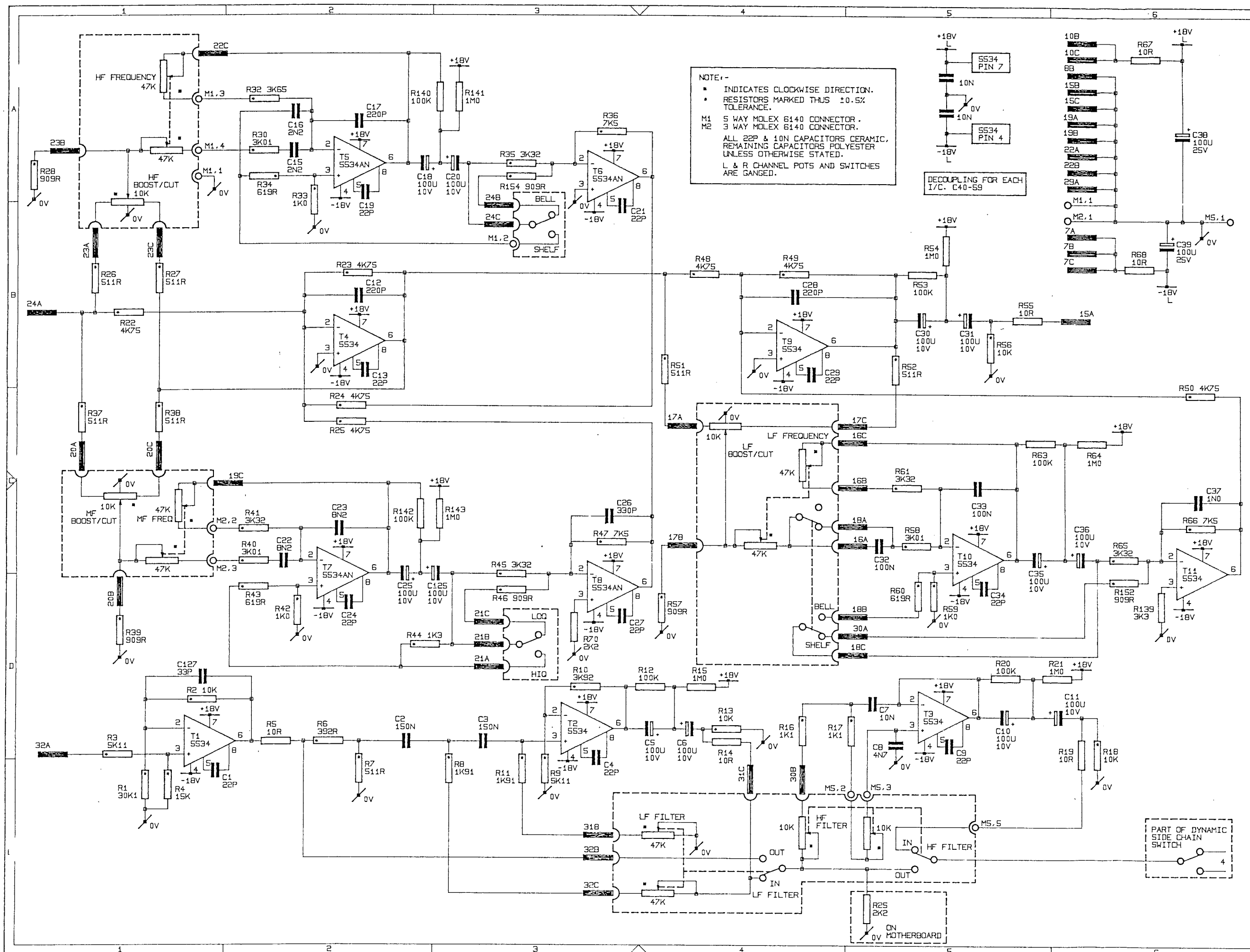


96 Way DIN 41612 Connector List					
From: 611S Mother board					
To: 82E202 Stereo Equaliser and filters					
No	Row A	No	Row B	No	Row C
1	R Eq output	1	0V (R)	1	0V (R)
2	R LF F (a) CW	2	R LF F (b) CW	2	R LF F (b) ACW + Wiper
3	R LF +/- CW	3	R LF +/- Wiper	3	R LF +/- ACW
4	R LF Shelf/Bell	4	R LF Shelf/Bell	4	R LF Shelf/Bell
5	0V (R)	5	0V (R)	5	R MF F (b) ACW + Wiper
6	R MF +/- CW	6	R MF +/- Wiper	6	R MF +/- ACW
7	Not used	7	-18V	7	-18V
8	Not used	8	0V (L)	8	0V (R)
9	Not Used	9	Not Used	9	R LF Shelf / Bell
10	Not used	10	+18V	10	+18V
11	R MF Hi Q	11	R MF HiQ	11	R MF HiQ
12	0V (R)	12	0V (R)	12	R HF F (b) ACW + Wiper
13	R HF +/- CW	13	R HF +/- Wiper	13	R HF +/- ACW
14	R Eq input	14	R HF Shelf/Bell	14	R HF Shelf/Bell
15	L Eq output	15	0V (L)	15	0V (L)
16	L LF F (a) CW	16	L LF F (b) CW	16	L LF F (b) ACW + Wiper
17	L LF F (a) CW	17	L LF +/- Wiper	17	L LF +/- ACW
18	L LF Shelf/Bell	18	L LF Shelf/Bell	18	L LF Shelf/Bell
19	0V (L)	19	0V (L)	19	L MF F (b) ACW + Wiper
20	L MF +/- CW	20	L MF +/- Wiper	20	L MF +/- ACW
21	L MF Hi Q	21	L MF Hi Q	21	L MF Hi Q
22	0V (L)	22	0V(L)	22	L HF F (b) ACW Wiper
23	L HF +/- CW	23	L HF +/- Wiper	23	L HF +/- ACW
24	L Eq input	24	L HF Shelf/Bell	24	L HF Shelf/Bell
25	0V (R)	25	R HF Filter output	25	R HF Filter F(b) ACW
26	Spare	26	R HF Filter (a) input	26	R HF Filter F(b)CW + Wpr
27	Spare	27	R LF Filter F(a) CW	27	R LF Filter output
28	R Filter buffer	28	R LF Filter input	28	R LF Filter F(b)ACW + Wpr
29	0V (L)	29	L HF Filter output	29	L HF Filter F(b) ACW
30	L LF Shelf / Bell	30	L HF Filter (a) input	30	L HF Filter F(b)CW + Wpr
31	Spare	31	L LF Filter F(a) CW	31	L LF Filter output
32	L Filter buffer	32	L LF Filter input	32	L LF Filter F(b)ACW + Wpr

T82202.50
04 Jan 85



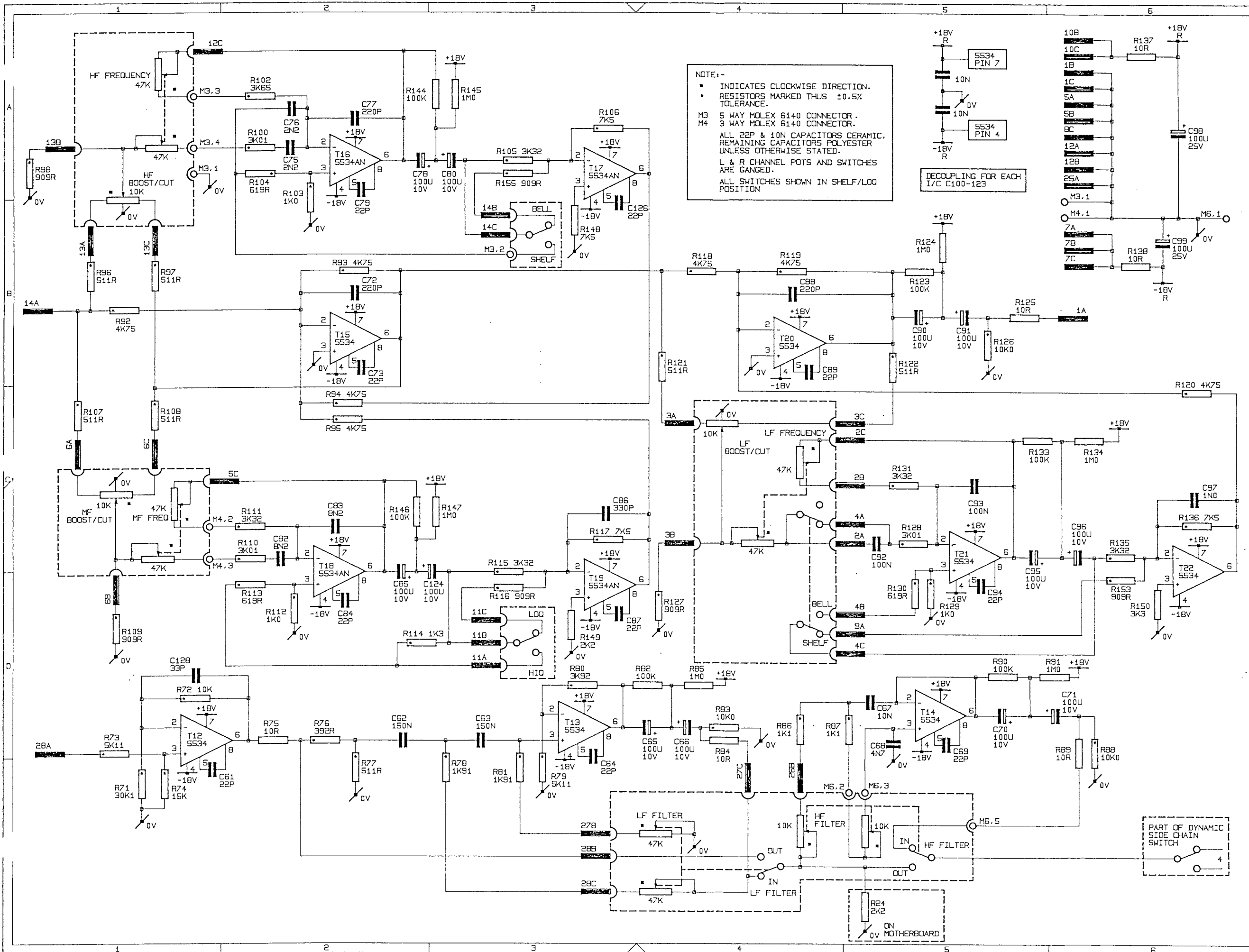
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REV	ISSUE	DATE	DETAILS
P	A		PROTOTYPE
1	A		HF, LF BELL Q INCREASED TO HF MF, F POTS TAKEN VIA MOLEX CON
2	A		R27, R39, R57 CHANGED
3	A		HF FILTER POTS TAKEN VIA MOLEX CONNECTORS. R86, R87 CHANGED TO 1K1
3	B		POT AND SWITCH NAMES ADDED SI
3	C	8C SEPT 86	REDRAWN SI
3	D	JUNE 87	POTS AND SWITCHES RE ARRANGED BC STEFF
4	A	23 MAR 88	ECO 4K/339 REVISION LEVEL UPDATED STEFF
4	B	12 JULY 89	ECON 18A NOW 18C TH STEFF
4	C	1 SEPT 89	ECO 4K/425 C127 33P ADDED STEFF
5	A	1 SEPT 89	ECO 4K/465 JKL REV BOARD INTRODUCED STEFF

SHEET 1 OF 2
REF: T611SB A2

TITLE: STEREO PARAMETRIC EQUALISER/FILTERS L CHANNEL

DRG. NO. T82202.70

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REV	ISSUE	DATE	DETAILS
P	A		PROTOTYPE
1	A		HF, LF BELL Q INCREASED TO HF MF, F POTS TAKEN VIA MOLEX CON
2	A		R98, 109-127 CHANGED
3	A		HF FILTER POTS TAKEN VIA MOLEX CONNECTORS. R96, R97 CHANGED TO 1K1
3	B		POT AND SWITCH NAMES ADDED
3	C	8C SEPT 86	REDRAWN
3	D	JUNE 87	POTS AND SWITCHES RE ARRANGED
4	A	23 MAR 88	ECO 4K/339 RESISTOR ADJ R105 NOW NUMBERED R155
4	B	12 JULY 89	C100 22P NOW C126
4	C	1 SEPT 89	ECO 4K/425 C128 33P ADDED
5	A	1 SEPT 89	ECO 4K/465 JKL REV BOARD INTRODUCED

SHEET 2 OF 2
REF: T6115A A2

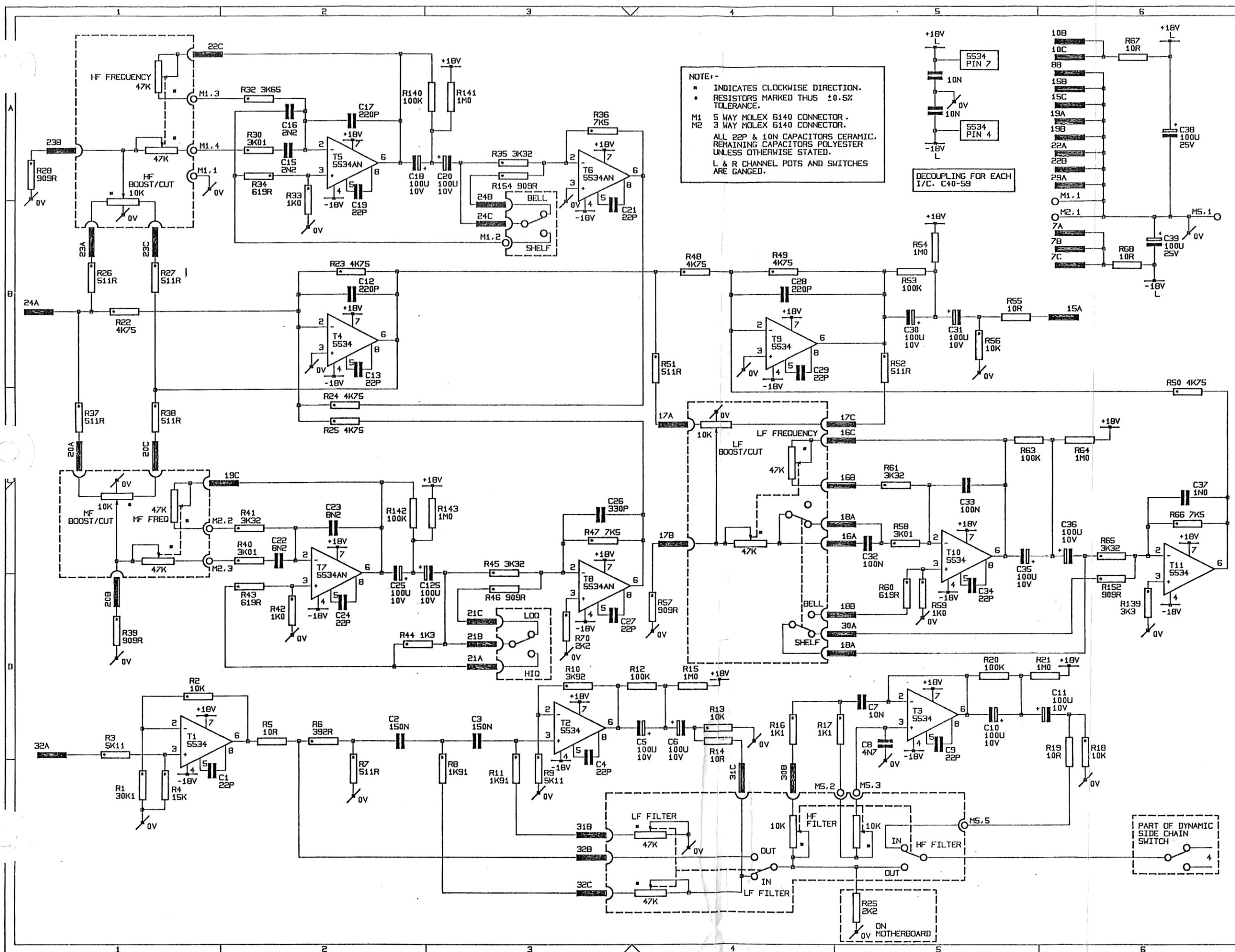
TITLE: STEREO PARAMETRIC EQUALISER/FILTERS R CHANNEL

DRG. NO.: T82202.71

Solid State Logic

96 Way DIN 41612 Connector List		
From: 611S Mother board		
To: 82E202 Stereo Equaliser and filters		
No	Row A	No Row B
1	R Eq output	1 0V (R)
2	R LF F (a) CW	2 R LF F (b) CW
3	R LF +/- CW	3 R LF +/- Wiper
4	R LF Shelf/Bell	4 R LF Shelf/Bell
5	0V (R)	5 0V (R)
6	R MF +/- CW	6 R MF +/- Wiper
7	Not used	7 -18V
8	Not used	8 0V (L)
9	Not Used	9 Not Used
10	Not used	10 +18V
11	R MF Hi Q	11 R MF HiQ
12	0V (R)	12 0V (R)
13	R HF +/- CW	13 R HF +/- Wiper
14	R Eq input	14 R HF Shelf/Bell
15	L Eq output	15 0V (L)
16	L LF F (a) CW	16 L LF F (b) CW
17	L LF F (a) CW	17 L LF +/- Wiper
18	L LF Shelf/Bell	18 L LF Shelf/Bell
19	0V (L)	19 0V (L)
20	L MF +/- CW	20 L MF +/- Wiper
21	L MF Hi Q	21 L MF Hi Q
22	0V (L)	22 0V(L)
23	L HF +/- CW	23 L HF +/- Wiper
24	L Eq input	24 L HF Shelf/Bell
25	0V (R)	25 R HF Filter output
26	Spare	26 R HF Filter (a) input
27	Spare	27 R LF Filter F(a) CW
28	R Filter buffer	28 R LF Filter input
29	0V (L)	29 L HF Filter output
30	L LF Shelf / Bell	30 L HF Filter (a) input
31	Spare	31 L LF Filter F(a) CW
32	L Filter buffer	32 L LF Filter input

T82202.50
04 Jan 85



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REV	ISSUE	DATE	DETAILS
P	A		PROTOTYPE
1	A		HF, LF BELL Q INCREASED TO HF MF, F POTS TAKEN VIA MOLEX CON
2	A		R27, R39, R57 CHANGED
3	A		HF FILTER POTS TAKEN VIA MOLEX CONNECTORS. R86, R87 CHANGED TO 1K1
3	B		POT AND SWITCH NAMES ADDED S1
3	C	8C SEPT 86	REDRAWN S1
3	D	JUNE 87	POTS AND SWITCHES RE ARRANGED BC STEFF
4	A	23 MAR 88	ECO 4K/339 REVISION LEVEL UPDATED STEFF

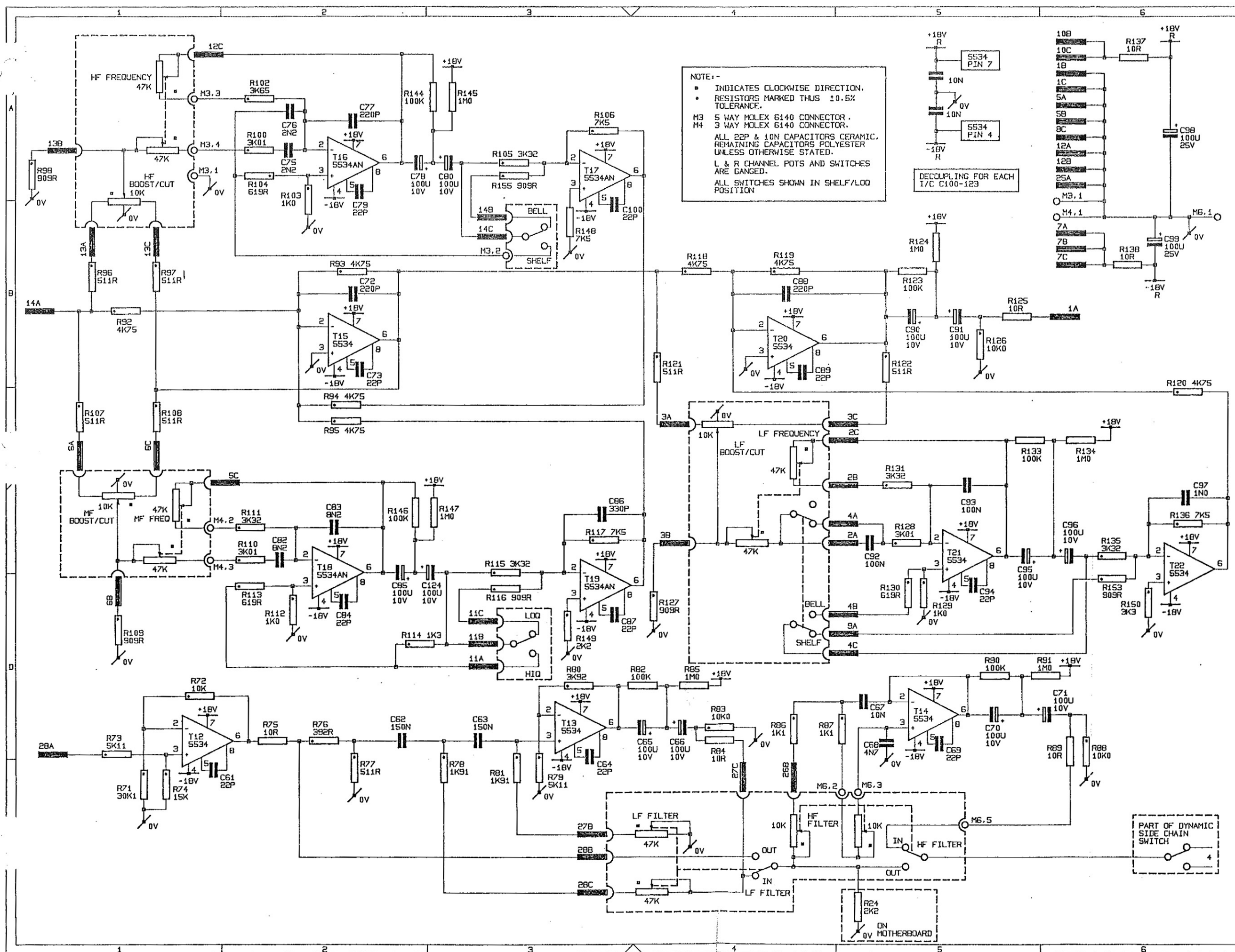
SHEET 1 OF 2

REF: T611SB A2

TITLE STEREO PARAMETRIC EQUALISER/FILTERS L CHANNEL

DRG. NO. T82202.70

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REV	ISSUE	DATE	DETAILS
P	A		PROTOTYPE
1	A		HF, LF BELL Q INCREASED TO HF MF, F POTS TAKEN VIA MOLEX CON
2	A		R98, 109, 127 CHANGED
3	A		HF FILTER POTS TAKEN VIA MOLEX CONNECTORS. R86, R87 CHANGED TO 1K1
3	B		POT AND SWITCH NAMES ADDED S1
3	C	BC SEPT 86	REDRAWN S1
3	D	JUNE 87	POTS AND SWITCHES RE ARRANGED BC STEFF
4	A	23 MAR 88	ECO 4K/339 RESISTOR ADJ R105 NOW NUMBERED R155 STEFF
SHEET 2 OF 2			
REF: T6115A A2			
TITLE STEREO PARAMETRIC EQUALISER/FILTERS R CHANNEL			
DRG. NO. T82202.71			
Solid State Logic			